



Chip Errata

DICE JR/Mini

Mask: 0806

Revision : 1.01

Date : 29-Jul-10

IMPORTANT NOTICE

TC Applied Technologies Ltd. ("TCAT") believes that the information contained herein was accurate and reliable at time of writing. However, the information is subject to change without notice and is provided "AS IS" without warranty of any kind (express or implied), and TCAT reserves the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time, and to discontinue any product or service without notice. Customers are advised to obtain the latest version of any and all relevant information to verify, before placing orders or beginning development of products based on TCAT technologies, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment, including those pertaining to warranty, indemnification, and limitation of liability.

No responsibility is assumed by TCAT for the use of this information, including use of this information as the basis for development, manufacture or sale of any items, or for infringement of patents or other rights of third parties. This document is the property of TCAT; by furnishing this information, TCAT grants no license, express or implied, under any patents, mask work rights, copyrights, trademarks, trade secrets or other intellectual property rights. TCAT owns the copyrights associated with the information contained herein and gives consent for copies to be made of the information only for use within your organization with respect to TCAT integrated circuits, software, design files and any other products of TCAT. This consent does not extend to other copying such as copying for general distribution, advertising or promotional purposes, or for creating any work for resale. Resale of TCAT products or services with statements different from or beyond the parameters stated by TCAT for that product or service voids all express and any implied warranties for the associated TCAT product or service and is an unfair and deceptive business practice. TCAT is not responsible or liable for any such statements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TCAT PRODUCTS ARE NOT DESIGNED, AUTHORIZED OR WARRANTED FOR USE IN AIRCRAFT SYSTEMS, MILITARY APPLICATIONS, PRODUCTS SURGICALLY IMPLANTED INTO THE BODY, AUTOMOTIVE SAFETY OR SECURITY DEVICES, LIFE SUPPORT PRODUCTS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TCAT PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK AND TCAT DISCLAIMS AND MAKES NO WARRANTY, EXPRESS, STATUTORY OR IMPLIED, INCLUDING THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR PARTICULAR PURPOSE, WITH REGARD TO ANY TCAT PRODUCT THAT IS USED IN SUCH A MANNER. IF THE CUSTOMER OR CUSTOMER'S CUSTOMER USES OR PERMITS THE USE OF TCAT PRODUCTS IN CRITICAL APPLICATIONS, CUSTOMER AGREES, BY SUCH USE, TO FULLY INDEMNIFY TCAT, ITS OFFICERS, DIRECTORS, EMPLOYEES, DISTRIBUTORS AND OTHER AGENTS FROM ANY AND ALL LIABILITY, INCLUDING ATTORNEYS' FEES AND COSTS, THAT MAY RESULT FROM OR ARISE IN CONNECTION WITH THESE USES.

TC Applied Technologies, TCAT and the TC Applied Technologies, DICE™ and JetPLL™ logo designs are trademarks of TC Applied Technologies Ltd. All other brand and product names in this document may be trademarks or service marks of their respective owners.

Errata Number	Errata description	Applies to mask
E1	<p>The SPI interface the TxDone interrupt can not be unmasked. The TxDone status bit will still indicate the 'done' status but it can not generate an interrupt.</p> <p>The firmware works around this problem by using the other interrupt sources.</p>	0806
E2	<p>The ADAT interface will not be able to be used as master for the JET PLL when the JET PLL is running on the clock doubler clock.</p> <p>The workaround is to run the JET PLL off the same clock as the ARM and DICE system. This is the default setting in firmware and this does provide the optimal jitter performance as well.</p>	0806
E3	<p>The Peak detector in the Router module will sometimes report a non existing peak of 0x202 if the peak register is read with interrupts enabled. This is a result of a conflict with another APB read.</p> <p>There are two workarounds for this problem:</p> <ol style="list-style-type: none"> 1) Disable interrupts while reading peak values 2) Disregard the value 0x202 from peaks. 	0806
E4	<p>AVS Rx Stuck state. In rare occasions the receiver will not be able to lock to an incoming 1394 stream. The firmware contains a daemon which makes the system recover automatically from this situation.</p>	0806
E5	<p>AVS Tx Stuck state. If one of the AVS receivers is turned off in the middle of receiving a packet the arbiter will prevent the AVS Tx from getting access to the bus until the next AVS packet is received. If no more packets are being received this deadlock will persist.</p> <p>A daemon has been implemented to resolve this problem in firmware.</p>	0806
E6	<p>The GPIO7 port is not controlled by the GPIO7 direction bit but by the GPIO6 direction bit. This does not affect the secondary function of the pin, only the GPIO functionality. In reality this means that if both GPIO6 and GPIO7 are used they must have same direction, i.e. both inputs or both outputs.</p>	0806
E7	<p>The hardware mixer does not produce the correct results at 176.4kHz and 192kHz sample rates. Every second sample is wrong. Please see: tc22x0MixerBug.pdf for a full description and for workarounds.</p>	0806